

TRANSMITTAL

Electronic Version v1.1

Stylesheet Version v1.1.0

Title of Invention	SPECIFIC SITE BACKSIDE UNDERLAYING AND MICROMASKING METHOD FOR ELECTRICAL CHARACTERIZATION OF SEMICONDUCTOR DEVICES	
Application Number :		
Date :		
First Named Applicant:	Barbara A. Averill	
Confirmation Number:		
Attorney Docket Number:	FIS920030275US1	
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Submitted By:	Elec. Sign.	Sign. Capacity
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Documents being submitted:

us-fee-sheet

us-ids

us-assignment

us-request

us-declaration

us-declaration

us-declaration

us-declaration

us-declaration

us-declaration

us-declaration

us-declaration

application-body

Files

ePAVE-usfees.xml

us-fee-sheet.xsl

us-fee-sheet.dtd

ePAVE-usidst.xml

us-ids.dtd

us-ids.xsl

ePAVE-usassn.xml

us-assignment.xsl

us-assignment.dtd

Assignmentp1.tif

Assignmentp2.tif

Assignmentp3.tif

Assignmentp4.tif

ePAVE-usrequ.xml

us-request.dtd

us-request.xsl

Declarationp1.tif

Declarationp2.tif

Declarationp3.tif

Declarationp4.tif

Declarationp5.tif

Declarationp6.tif

Declarationp7.tif

Declarationp8.tif

Specification-trans.xml

us-application-body.xsl

application-body.dtd

wipo.ent

mathml2.dtd

mathml2-qname-1.mod

isoamsa.ent

isoamsb.ent

isoamsc.ent

isoamsn.ent

isoamso.ent

isoamsr.ent

isogrk3.ent

isomfrk.ent

isomopf.ent

isomscr.ent

isotech.ent

isobox.ent

isocyr1.ent

isocyr2.ent

isodia.ent

isolat1.ent

isolat2.ent

isonum.ent

isopub.ent

mmlextra.ent
mmlalias.ent
soextblk.dtd
Figure1.tif
Figures234.tif
Figures65.tif

Comments